

CLAIMS

1. A stereo demodulator circuit comprising at least one noise control unit for performing a noise control 5 responding to a reception electric-field intensity when said reception electric-field intensity is within a specified range, further comprising:

an AD converter unit for AD-converting a reception electric-field intensity signal indicating said 10 reception electric-field intensity;

an offset unit for digitally offsetting a digital signal obtained from said AD converter unit by a predefined value according to said specified range and truncating lower bits off said digital signal by the 15 number of bits specified in compliance with a grade of noise control accuracy performed in said noise control unit; and

a control signal output unit for outputting a control signal defining a control variable of a noise 20 control performed in said noise control unit based on a signal obtained from said offset unit.

2. The stereo demodulator circuit in claim 1, wherein said noise control unit is switched stepwise for

providing a noise control variable responding to a control signal outputted from said control signal output unit.

5 3. A stereo demodulator circuit comprising at least one noise control unit for performing a noise control responding to a reception electric-field intensity when said reception electric-field intensity is within a specified range, further comprising:

10 an offset unit for offsetting a reception electric-field intensity signal indicating said reception electric-field intensity by a predefined value according to said specified range;

15 a difference output unit for comparing a signal obtained from said offset unit with a zero bias and outputting the resultant difference; and

20 a control signal output unit for outputting a control signal defining a noise control variable for said noise control unit based on a signal obtained from the difference output unit.

4. The stereo demodulator circuit in any one of claims 1 to 3 further comprising a plurality of said noise control units, wherein said specified range is respectively

specified for each of the plurality of noise control units.

5. A signal processing circuit comprising at least one circuit part performing a prescribed control responding to an input signal level when said input signal level is within a specified range, further comprising:

10 an AD converter unit for AD-converting a level signal which is a signal indicating said input signal level;

15 an offset unit for digitally offsetting a digital signal obtained from the AD converter unit by a predefined value according to said specified range and truncating lower bits off said digital signal by the number of bits specified in compliance with a grade of said prescribed control accuracy performed in said circuit part; and

20 a control signal output unit for outputting a control signal defining a control variable of said prescribed control performed in said circuit part based on a signal obtained from the offset unit.

6. A signal processing circuit comprising at least one circuit part performing a prescribed control responding to an input signal level when said input signal

level is within a specified range, further comprising:

an offset unit for offsetting a level signal which is a signal indicating said input signal level by a predefined value according to said specified range;

5 a difference output unit for comparing a signal obtained from the offset unit with a zero bias and outputting the resultant difference; and

10 a control signal output unit for outputting a control signal defining a control variable of said control performed in said circuit part based on a signal obtained from the difference output unit.